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TRANSMITTAL FORM

Application Serial Number	10/788,741
Filing Date	February 27, 2004
First Named Inventor	Lee
Group Art Unit	2813
Examiner Name	Nguyen, Tuan H.
Attorney Docket No.	ASC-012DV
Patent No.	Not applicable
Issue Date	Not applicable

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Check Attached <input type="checkbox"/> Copy of Fee Transmittal Form <input type="checkbox"/> Amendment/Response <input type="checkbox"/> Preliminary <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Letter to Official Draftsperson including Drawings [Total Sheets <input type="text"/> <input type="checkbox"/> Petition for Extension of Time <input checked="" type="checkbox"/> Supplemental Information Disclosure Statement <input checked="" type="checkbox"/> Form PTO SB/08a/b citing references A73-A95, B30 and C75-C82 <input checked="" type="checkbox"/> Copies of IDS Citations B30 and C75-C82 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Sequence Listing submission <input type="checkbox"/> Paper Copy/CD <input type="checkbox"/> Computer Readable Copy <input type="checkbox"/> Statement verifying identity of above	<input type="checkbox"/> Copy of Notice to File Missing Parts of Application <input type="checkbox"/> Formal Drawing(s) <input type="checkbox"/> Request For Continued Examination (RCE) Transmittal <input type="checkbox"/> Power of Attorney (Revocation of Prior Powers) <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Declaration and Power of Attorney for Utility or Design Patent Application <input type="checkbox"/> Small Entity Statement <input type="checkbox"/> CD(s) for large table or computer program <input type="checkbox"/> Amendment After Allowance <input type="checkbox"/> Request for Certificate of Correction <input type="checkbox"/> Certificate of Correction (in duplicate)	<input type="checkbox"/> Notice of Appeal to Board of Patent Appeals and Interferences <input type="checkbox"/> Appeal Brief (in triplicate) <input type="checkbox"/> Status Inquiry <input checked="" type="checkbox"/> Return Receipt Postcard <input checked="" type="checkbox"/> Certificate of First Class Mailing under 37 C.F.R. 1.8 <input type="checkbox"/> Certificate of Facsimile Transmission under 37 C.F.R. 1.8 <input type="checkbox"/> Additional Enclosure(s) (please identify below)
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CORRESPONDENCE ADDRESS	SIGNATURE BLOCK
Direct all correspondence to: Patent Administrator Goodwin Procter LLP Exchange Place Boston, MA 02109 Tel. No.: (617) 570-1000 Fax No.: (617) 523-1231 Customer No. 051414	Respectfully submitted, Date: October 10, 2006 Reg. No. 44,381 Tel. No.: (617) 570-1806 Fax No.: (617) 523-1231 Natasha C. Us Attorney for Applicants Goodwin Procter LLP Exchange Place Boston, MA 02109



PATENT
Attorney Docket No. ASC-012DV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): *Lee et al.*
SERIAL NO.: 10/788,741 GROUP NO.: 2813
FILING DATE: February 27, 2004 EXAMINER: Nguyen, Tuan H.
TITLE: STRUCTURES WITH PLANAR STRAINED LAYERS

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P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The references listed on the enclosed PTO SB/08a/b are submitted solely in compliance with the duty of candor. It is understood that this Supplemental Information Disclosure Statement does not fall within the provisions of 37 C.F.R. §1.97.

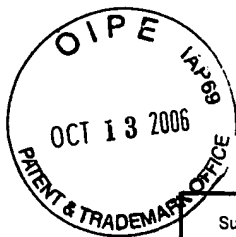
It is respectfully requested that the patents and publications listed on the attached PTO SB/08a/b, and other information contained herein, be made of record in this application.

Respectfully submitted,

Date: October 10, 2006
Reg. No. 44,381

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PTO/SB/08A/B (09-06)

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				Application Number	10/788,741
				Filing Date	February 27, 2004
				First Named Inventor	Lee
				Art Unit	2813
				Examiner Name	Nguyen, Tuan H.
Sheet	1	of	2	Attorney Docket Number	ASC-012DV

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Filing Date if Appropriate
		Number-Kind Code ² (if known)			
	A73	4,497,683	02-05-1985		
	A74	5,266,521	11-30-1993		
	A75	5,479,033	12-26-1995		
	A76	6,437,375	08-20-2002		06-05-2000
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	A93	2003/0057439	12-13-2005		08-09-2002
	A94	2003/0227013	12-11-2003		06-07-2002
	A95	2004/0026765	02-12-2004		06-06-2003

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			
	B30	0 829 908 A2 (EP)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C75	"2 Bit/Cell EEPROM Cell Using Band-to-Band Tunneling for Data Read-Out," IBM Technical Disclosure Bulletin, Vol. 35, No. 4B (September 1992) pp. 136-140.	
	C76	Bouillon <i>et al.</i> , "Search for the optimal channel architecture for 0.18/0.12 μ m bulk CMOS Experimental study," IEEE, (1996) pp. 21.2.1-21.2.4.	

Examiner Signature		Date Considered	
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
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Sheet	2	of	2	Attorney Docket Number	ASC-012DV

	C77	Grützmacher <i>et al.</i> , "Ge segregation in SiGe/Si heterostructures and its dependence on deposition technique and growth atmosphere," Applied Physics Letters, Vol. 63, No. 18 (November 1, 1993) pp. 2531-2533.	
	C78	International Search Report for PCT/US03/17275, dated October 14, 2003.	
	C79	International Search Report for PCT/US 03/181123, dated Dec. 1, 2003.	
	C80	Koester <i>et al.</i> , "Extremely High Transconductance Ge/Si _{0.4} Ge _{0.6} p-MODFET's Grown by UHV-CVD," IEEE Electron Device Letters, Vol. 21, No. 3 (March 2000) pp. 110-112.	
	C81	Maiti <i>et al.</i> , "Strained-Si heterostructure field effect transistors," Semicond. Sci. Technol., Vol. 13 (1998) pp. 1225-1246.	
	C82	Mizuno <i>et al.</i> , "Electron and Hole Mobility Enhancement in Strained-Si MOSFET's on SiGe-on-Insulator Substrates Fabricated by SIMOX Technology," IEEE Electron Device Letters, Vol. 21, No. 5 (May 2000) pp. 230-232.	

LIBC/2856997

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Attorney Docket No. ASC-012DV

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TITLE: STRUCTURES WITH PLANAR STRAINED LAYERS

CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence, and any document(s) referred to as enclosed herein, is/are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10th day of October, 2006.


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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are:

1. Transmittal Form (1 page);
2. Supplemental Information Disclosure Statement (1 page);
3. PTO Form SB/08a/b (2 pages) citing references A73-A95, B30 and C75-C82;
4. Copies of cited references B30 and C75-C82; and
5. Return receipt postcard

LIBC/2856959